

CAUTION
This product requires a PXI/cPCI Chassis with replaceable card guides per the Eurocard mechanical specification

PX465S 100MHz Pulse Generator

The PX465S is a fully programmable pulse generator that allows the generation of precisely timed pulses of programmable frequency, pulse width, delay, and amplitude. Operational modes include single, continuous, burst, and follow trigger modes. Extensive trigger and gating logic provides comprehensive control of pulse timing and the internal base clock can be disciplined to an external reference clock.

This unit also features a position for adding one single-wide M-Module.

Overview:

Number of Channels: 1
Frequency: 0.1Hz - 100MHz
Pulse Output: -1.5V to +6.5V

Operational Modes:

- Single or continuous pulsing
- Single pulse or double pulse
- Burst from 2 to >1B pulses
- Follow trigger
- Inverted Pulse/Sync
- Programmable rise/fall time
- External triggering
- Asynchronous or synchronous gating

Clock Disciplining:

Internal clock can be disciplined to a 10MHz external reference for increased accuracy and stability

Inputs/Outputs:

- Front Panel Pulse & Sync Out
- Front Panel Input A & B
- M-Module Trig A & B

Gate, Trigger, Ref. Clock Inputs:

Source can be the front panel A or B connectors or the M-Module Trig A or Trig B signals

Pulse and Sync Outputs:

Can be directed to the front panel connectors and to the M-Module backplane Trig A or B signals

Front Panel Connectors: SMA

Ordering Information

Part Number 11030240-0001

CPCI/PXI Compliance

Complies with PCI spec. 2.0 R3.0 and PCI spec 2.2

5V and 3.3V signaling voltage (VIO) supported

5V only power supply

33MHz PCI data bus

Five trigger lines compliant with PXI Specification 2.1

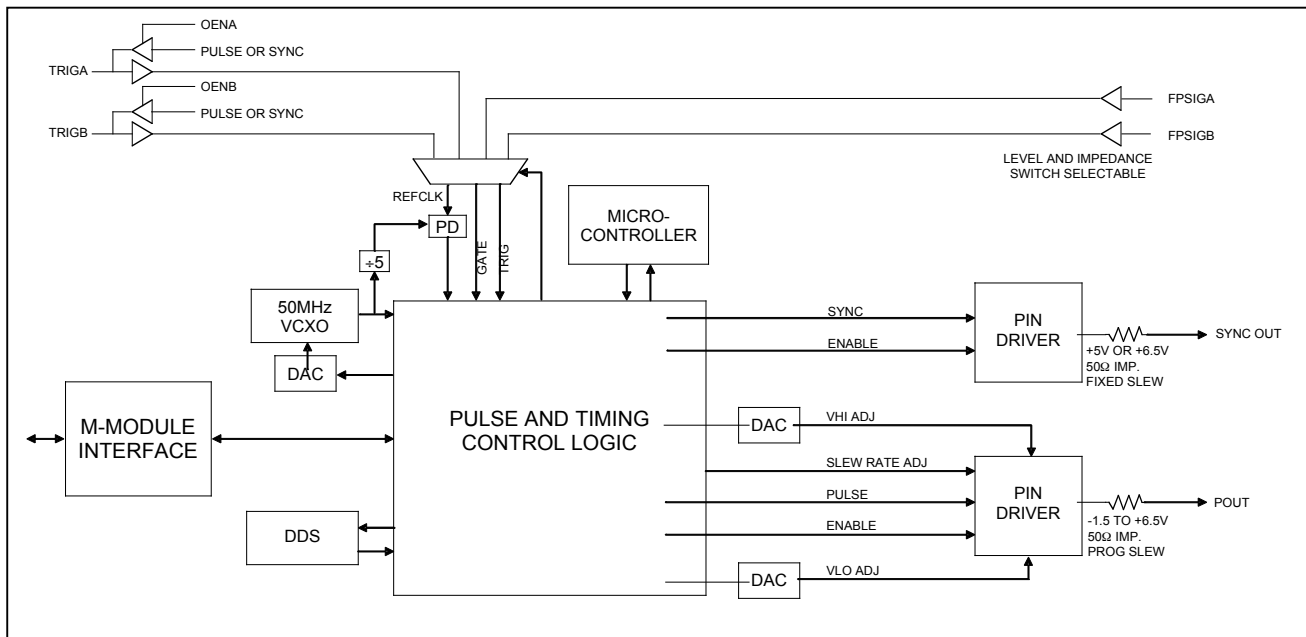
Form Factor: 3U

Applications

- Functional testing
- Design verification
- Signal simulation
- Timing control

Additional Information

User Manuals for C&H carriers and this module can be found on our website at www.chtech.com.



Specifications:

Pulse Frequency (internal clock):

Range	0.093Hz to 100MHz
Resolution	0.093Hz
Accuracy	$\pm 0.01\%$ ¹

Pulse Width:

Range	5ns to (period - 3ns)
Accuracy	$\pm(3\% + 250ps)$ ²

Pulse Delay (from Sync Out):

Range	5ns ³ to 5s
Accuracy	$\pm(3\% + 250ps)$ ²

Double Pulse Spacing:

Range	(width + 3ns) to (period - width - 3ns)
Accuracy	$\pm(3\% + 250ps)$ ²

Timing (Width, Delay, Double Spacing):

Resolution	4 to 13ns	10ps
	13 to 24ns	20ps
	$\geq 24ns$	see note 4
Temperature Coefficient	$+17ppm/^{\circ}C$ typ	

Pulse Out Characteristics:

Range ($R_L = \infty$)	-1.5V to +6.5V
Output Impedance	50 Ω
Resolution (12 bit)	2mV
Accuracy	$\pm(2.0\% + 100mV)$
Output Current (source or sink)	60mA
Short Circuit Current (dynamic)	$\pm 120mA$ max
Rise/Fall Time (prog, $R_L = \infty$)	0.625 to 2.5V/ns

Temperature:

Operating	0 $^{\circ}C$ to 50 $^{\circ}C$
Storage	-40 $^{\circ}C$ to 70 $^{\circ}C$

Input Characteristics (FPSIGA & FPSIGB):

Threshold (programmable)	-5.0V to +5.0V
Resolution (8 bit)	39mV
Impedance (selectable)	50 or >10K Ω
Accuracy (mid-point falling/rising)	$\pm(5\% + 150mV)$
Hysteresis	50-350mVpp
Frequency	100MHz max
Pulse Width	3ns min
Maximum Input Voltage (no damage)	$\pm 12V_{rms}$

Sync Out Characteristics:

Time from external trigger	<35ns ⁵
Output Impedance	50 Ω
Amplitude (selectable, $R_L = \infty$)	5.0V or 6.5V
Output Current (source or sink)	60mA
Rise/Fall Time ($R_L = \infty$)	2.5V/ns typ
Width (software selectable)	5ns or 55ns

Power:

+5V	1.9 A max
+12V	0.4 A max
-12V	0.33 A max

Notes:

1. The percent accuracy can be improved by disciplining the internal clock to an external precision 10MHz reference clock. The internal clock accuracy will discipline in about 10 minutes to within one decade of the external reference, up to 10^{-8} accuracy.
2. Accuracy is within the tolerance specified at the calibration temperature. Temperature coefficient can be used to correct for temperature variation.
3. The pulse delay can be programmed to zero; however, the minimum Sync Out time to output pulse specification applies.
4. For times $\geq 24ns$, the timing resolution increases approx. 10ps for every 12ns increase in time.
5. The Sync Out time specified is from the external front panel trigger to the external front panel Sync Out. Backplane trigger timing will vary depending on the carrier used.